

CAT1 Media	CAT2 Scope	CAT3 Genre	Manufacturer (ACME :)	↓ TI-99/4x - PART: by schmitzi	HOUSE or requires	LABEL or Chip	P/N	RARI	YEAR my	COMMENTS
DSK	TOOL	MENU	Tim T.	9640 Menu System (2015 Beta)	F18Areq or 9	80COL-F1	?	DL	2015	CLOCK - F18A o. 9938 NEEDED ! came with Tim T.'s "Super Mass Transfer" - CLOCK-Tool -Compatible with: CorComp
DSK	TOOL	MENU	Johnson John A.	BOOT	-	80COL/F1	?	DL	?	
DISK	TOOL	DiskMan	Myarc	Disk Manager 5 v1.5	?	XB2.7	?	4 : RA	?	_ CLOCK
DSK	TOOL	DiskUtil	Birdwell John	Disk Utilities v4.2 (DSKU)	?	XB2.7	?	DL	?	Clock
DSK	TELCO	BBS	Schneider Chris	FuSiON BBS for TI-99/4a & Geneve Home computers	9640	?	-	DL	?	using S&T Machine code, Clock CorComp TripleTech Geneve etc I have it even coded
DSK	TOOL	MENU	Johnson John A.	BOOT	-	80COL/F1	?	DL	?	- CLOCK-Tool
DSK	TOOL	Calender	Johnson John A.	REMIND ME!	?	?	?	DL	?	_ - CLOCK-Tool Compatible with: CorComp, MBP, Mvarc 9640
DSK	TOOL	Clock	Knerr Tony	SETCLOCK	?	?	-	DL	?	CLOCK
DSK	TELCO	BBS	Shields Mark/Schneider Cl	SOFTWORX BBS latest (uses the Zyolog BBS Machine Lan	9640	?	-	DL	?	(uses the Zyolog BBS Machine Language), Clock?
DISK	TELCO	BBS	Tim T.	S&T BBS	9640	?	-	4 : RA	?	Clock
CART	PROG	Basic	Triton	Super Extended BASIC (SXB) (MillerGraphics?)	?	?KB	TRI-BDAK	6 : ER	1987	- CLOCK ?
CS1	EDU	?	Maple Leaf MicroWare	TIME TUTOR (Stainless Software) (UK)	TiB	-	?	4 : RA	?	Another excellent educational program from Canada teaches time telling from o'clock
HW				HARDWARE & OPTIONS						
PEBC	CTRL	FDD	SNUG	BwG - FloppyDisk-Controller w/clock	9640	?	BwG	4 : RA	1990	CRU >1100 dyn? - CLOCK - 4xFDD DS/DD WD1773-chip_Clock=TripleTech-compatible
SIDE	PEB	Housing	CorComp	CES ????	?	16KB	?	4 : RA	?	CRU >???? - CLOCK
SIDE	CLOCK	Chip	CorComp	CC 9900 Clock (Standalone)	?	?	?	4 : RA	?	CRU >???? - CLOCK - Gazoo: I believe the Corcomp clock to be like the Param clock
PEBC	CLOCK	Chip	CorComp	CC 9900 TripleTech Clock PRT-Buffer Speed	?	2KB	YKAG	4 : RA	?	CRU >1D00 fix - CLOCK
HW	CPU	UNIT	CorComp	CC 99/64 (aka Phoenix) (64K-1M, RS232)	?	?	?	7 : NR	?	CLOCK - CRU >??
PEBC	CPU	UNIT	Myarc	Geneve 9640 Original Model (M-DOS 0.97) (TMS9938)	9640	16KB/80c	?	4 : RA	1987	- CLOCK (Battery), 12MHz, 512KB RAM, V9938 w/ 128KB VDP-Ram_RGB analog &
PEBC	CPU	UNIT	Walters Ron G. - Dynamic	Geneve 9640 with GENmod	9640	16KB/80c	?	5 : VR	?	- CLOCK - by Walters Ron G. - Dynamic Systems Research - Horizon - Bud Mill
PEBC	CPU	UNIT	Cecure	Geneve 9640 with PFMmod -384K 0-wait st	9640	16KB/80c	?	5 : VR	?	- CLOCK
PEBC	CPU	UNIT	Cecure	Geneve 9640 with PFMmod -512K 0-wait st	9640	16KB/80c	?	5 : VR	?	- CLOCK
EPROM	CPU	ROM-UPG	Cecure	Geneve 9640 - The original ROM-based Boot	9640	MOD	?	4 : RA	?	- CLOCK
EPROM	CPU	ROM-UPG	Walters Ron G.	Geneve 9640 - The GENmod (for Memex&Geneve96	9640	MOD	?	4 : RA	?	- CLOCK
EPROM	CPU	ROM-UPG	Cecure	Geneve 9640 - The PFMmod	9640	MOD	?	4 : RA	?	- CLOCK
EPROM	CPU	ROM-UPG	Cecure	Geneve 9640 - The PFMmod512	9640	MOD	?	4 : RA	?	- CLOCK
PEBC	DRIVE	GRAM	Horizon/Bud Mills	p-GRAM-Card (GROM-Emulator)	?	?	-	4 : RA	1988	CLOCK
PEBC	DRIVE	GRAM	Horizon/Bud Mills	p-GRAM-Card + (GROM-Emulator & Realtime Clock)	?	?	-	4 : RA	1988	CLOCK - CRU >?? - NOT FOR QI-Console
PEBC	CLOCK	Chip	DaTaBioTics	Grand RAM Realtime Clock	?	?	?	6 : ER	1987	CRU >???? dyn. - CLOCK
PEBC	CLOCK	GRAM	DaTaBioTics	Grand RAM GRAM-Board	?	?	?	7 : NR	1987	CRU >???? dyn. - CLOCK
PEBC	CTRL	MFM	Myarc	HFDC MFM-HDD/FDD-Controller (ST-506)	9640	16KB	HFDC	4 : RA	?	CRU >1000-1F00 16xdyn - CLOCK - max 402 (3x134) MB

