

BIN-FILES (TI-99)

BINARY	ACME	Kb TYPE	KB	PINS	Chips	-	TYPE	BOARD	COMMENT/VERSIONS	-
BINARY	CorComp	2764	8	28	1	-	MOD	CC-256K Buffer V2.1	minor corrections	
BINARY	CorComp	2764	8	28	1	-	ORG	CC-9900 FDC	-	
BINARY	CorComp	2764	8	28	2	-	ORG	CC-9900 FDC	Bank 1&2 (2 chips)	
BINARY	CorComp	2764	8	28	2	-	MOD	CC-9900 FDC	v8.9/MG - no startup-screen & new CALLs	
BINARY	CorComp	2764	8	28	1	-	MOD	CC-9900 FDC	v8.9/MG - DSK4-remove	
BINARY	CorComp	D27128D	16	28	2	-	ORG	CC Memory+	-	
BINARY	CorComp	D27128D	16	28	1	-	MOD	CC-9900	-	
BINARY	CorComp	D27128D	16	28	1	-	ORG	CC-9900 CES	-	
BINARY	CorComp	D27128D	16	28	1	-	MOD	CC-9900 CES-FIX	no startup-screen	
BINARY	CorComp	2732	4	24	1	-	ORG	CC-RS232 - 2732 2764	-	
BINARY	CorComp	2716	2	24	1	-	ORG	CC-TripleTech	-	
BINARY	CorComp	2764	8	28	1	-	ORG	CC-9900 RS232	-	
BINARY	CorComp	2764	8	28	1	-	ORG	TI-IBM-Transfer/IBM-Connection	-	
BINARY	DIJIT	2732	4	24	1	-	MOD	AVPC - 2732 2764	v2.0 - Upgrade	
BINARY	DIJIT/KNERR	2732	4	24	1	-	MOD	AVPC - 2732 2764	FCTN = is back	
BINARY	Foundation	2764	8	28	1	-	MOD	128K Card XB212	for XB-212	
BINARY	HamSoft	2764	8	28	1	-	ORG	Hamsoft	-	
BINARY	MechaTronic	2764	8	28	1	-	MOD	Mechatronics 80	80 v3.5	
BINARY	MechaTronic	2764	8	28	1	-	MOD	Mechatronics 80	80 Boone	
BINARY	MILLER GRAPHICS	2764	8	28	1	-	ORG	GRAMulator	v1.1	
BINARY	MILLER GRAPHICS	2764	8	28	1	-	MOD	GRAMulator	v2.0	
BINARY	MILLER GRAPHICS	2764	8	28	1	-	ORG	GRAMulator	v2.0 (Arcade)	
BINARY	MyArc	2764	8	28	1	-	ORG	128K Card	-	
BINARY	MyArc	2764	8	28	1	-	MOD	128K Card XB212	for XB-212	
BINARY	MyArc	2764	8	28	1	-	ORG	512K Card	-	
BINARY	MyArc	2764	8	28	1	-	ORG	512K Card	v1.3B - Intermediate UP	
BINARY	MyArc	2764	8	28	1	-	ORG	512K Card	v2.1 - Final	
BINARY	MyArc	2764	8	28	1	-	ORG	512K Card	v JD - Intermediate UP	
BINARY	MyArc	2764	8	28	1	-	ORG	DiskBackup WDS-HRD (maybe Gene	-	
BINARY	MyArc	2764	8	28	1	-	ORG	FDC DDCC-1 ()	v1.4 - STD	
BINARY	MyArc	2764	8	28	1	-	MOD	FDC DDCC-1 ()	v?.? - 80-Track	
BINARY	MyArc	2764	8	28	1	-	MOD	FDC DDCC-1 ()	v1.5 - UP	
BINARY	MyArc	2764	8	28	1	-	MOD	FDC DDCC-1 ()	v1.5 - 80 Track	
BINARY	MyArc	2764	8	28	1	-	MOD	FDC DDCC-1 ()	v1.6 - UP	
BINARY	MyArc	2764	8	28	1	-	ORG	DiskBackup WDS-HRD	-	
BINARY	MyArc/Cecure/Tim T.	D27128D	16	28	2	?	ORG	GENEVE Boot	v0.97 -	MF
BINARY	MyArc/Cecure/Tim T.	D27128D	16	28	2	?	ORG	GENEVE Boot	v0.98 - MDos2.21 (no SCSI)	MF
BINARY	Knerr Tony	D27128D	16	28	2	?	MOD	GENEVE Boot	v1.0 - boots from SCSI / new menu	SCSI
BINARY	MyArc/??	D27128D	16	28	2	?	MOD	GENEVE Boot	v1.1 - for GENmod (same as 1.0)	SCSI
BINARY	MyArc/??	D27128D	16	28	2	?	MOD	GENEVE Boot	v2.0 - FDD & HFDC (?) (32K)	SCSI

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BINARY	Cecure/??	D27128D	16	28	2	?	MOD	GENEVE Boot	v3.0.0 - PFMmod - MDOS??.? - FlashDisk 128K? - ORIG-BOARD	SCSI
BINARY	Cecure/??	D27128D	16	28	2	?	MOD	GENEVE Boot	v3.0.2 - PFMmod - MDOS5.0 - FlashDisk 128K? - 32K-SRAM - 192-384K-VDP-RAM	SCSI
BINARY	Cecure/??	D27128D	16	28	2	?	MOD	GENEVE Boot	v3.0.3 - PFMmod - MDOS5.0 - FlashDisk 128K? - 32K-SRAM - 192-384K-VDP-RAM - FDD 1.44MB	SCSI
BINARY	Cecure/Schroeder John	D27128D	16	28	2	1990+	MOD	GENEVE Boot	v3.0.4 - PFMmod - MDOS5.0 - FlashDisk 128K - 32K-SRAM - 192-384K-VDP-RAM-(schmitzi's Geneve)	SCSI
BINARY	MyArc	D27128D	16	28	1	-	ORG	HFDC ()	v1.1	
BINARY	MyArc	D27128D	16	28	1	-	MOD	HFDC ()	v1.4 - FDD-headstep 8ms and 4ms	
BINARY	MyArc	D27128D	16	28	1	-	MOD	HFDC ()	v1.4 - FDD-only	
BINARY	MyArc	D27128D	16	28	1	-	MOD	HFDC ()	v1.5 - HDD-only	
BINARY	MyArc	2732	4	24	1	-	ORG	RS232	-	
BINARY	MyArc	2732	4	24	1	-	MOD	RS232 (AVPC Mod)	Eliminates interrupt line conflict with AVPC	
BINARY	Percom	2764	8	28	1	-	ORG	FDC	-	
BINARY	Stewart Lee	27256	32	28	1	-	ORG	fbForth 2.0	-	
BINARY	TI	2532	4	24	2	-	ORG	Console Odd&Even	TI-99/4A	
BINARY	TI	2532	4	24	2	-	ORG	TI-FDC	-	
BINARY	TI	2532	4	24	2	-	MOD	TI-FDC 40/80/80	-	
BINARY	TI	2532	4	24	2	-	MOD	TI-FDC 40/40/80	-	
BINARY	TI	2532	4	24	2	-	MOD	TI-FDC 80/80/40	-	
BINARY	TI	2532	4	24	2	-	MOD	TI-FDC 80/80/80	-	
BINARY	TI	2532	4	24	2	-	MOD	TI-FDC Guion "A"	DSK4 w/ 12 or 20 ms - Adds DSK4 and lowercase drive designatio	
BINARY	TI	2532	4	24	2	-	MOD	TI-FDC Guion "B"	DSK4 w/ 12 or 20 ms - Adds DSK4 and lowercase drive designatio	
BINARY	TI	2564	8	24	1	-	ORG	TI-FDC DSDD	-	
BINARY	TI	2764	8	28	1	-	ORG	GRAM-Kracker-Converter	-	
BINARY	TI	D27128D	16	28	1	-	ORG	Neverlander	ReadOut of Game	
BINARY	TI	2532	4	24	1	-	ORG	RS232	-	
BINARY	TI	2532	4	24	1	-	MOD	RS232 (AVPC Mod)	Eliminates interrupt line conflict with AVPC	
BINARY	TI	2532	4	24	1	-	MOD	RS232 (Guion Mod)	Serial port = SIO BA=4800 PAR=710 TP=PAR	
BINARY	TI	2532	4	24	1	-	MOD	RS232 (HighSpeed)	1200-38.4Kbps	
BINARY	TI	2532	4	24	1	-	MOD	RS232 (Overclocked)	for 3.58-mHz clock	
BINARY	???	D27128D	16	28	1	-	ORG	SON of a Board (SOB)	80 Col original	
BINARY	???	D27128D	16	28	1	-	MOD	SON of a Board (SOB)	40 Col patch	